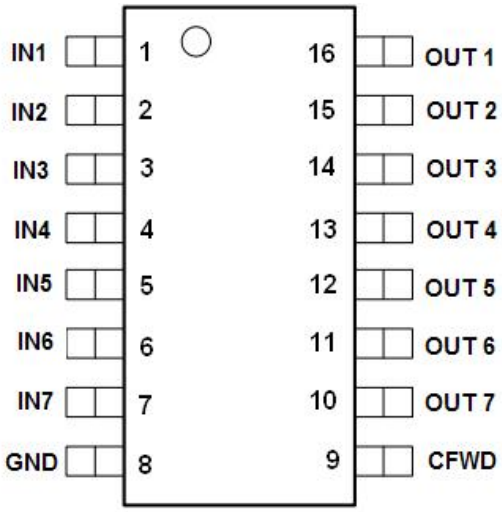
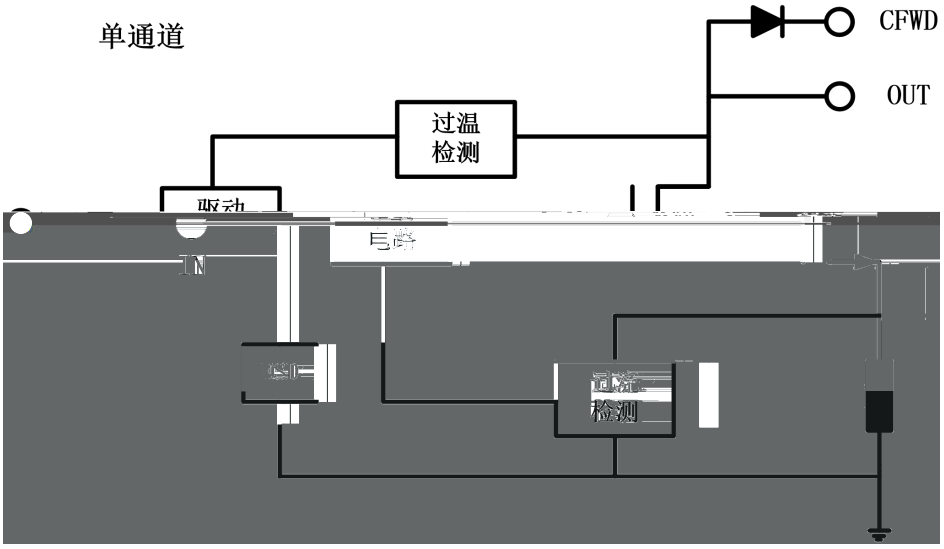
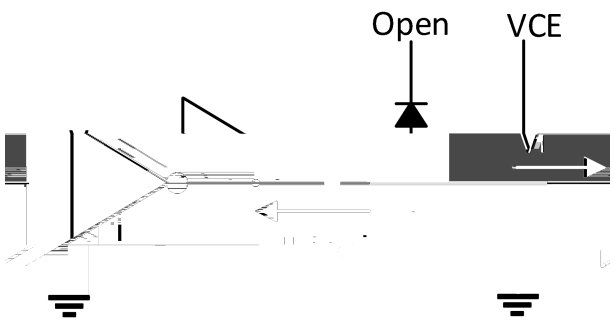
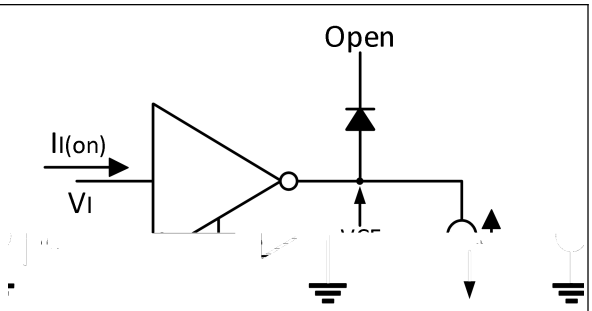
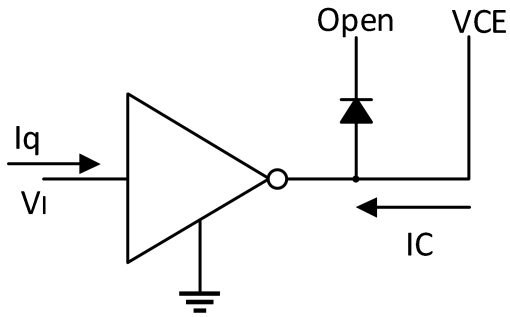
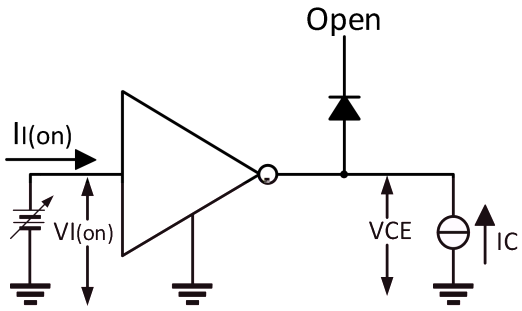

IN1
1
IN2
2
IN3
3
IN4
4
IN5
5
IN6
6
IN7
7
GND
8
16
OUT 1
15
OUT 2
14
OUT 3
13
OUT 4
12
OUT 5
11
OUT 6
10
OUT 7
9
CFWD

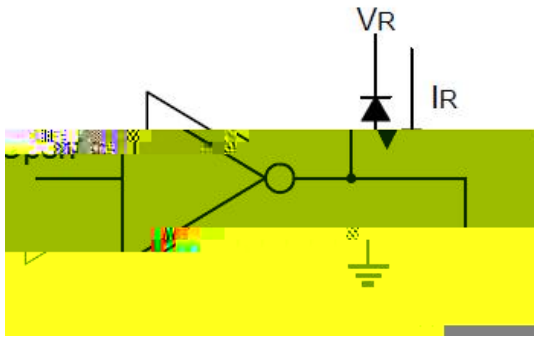

单通道
CFWD
OUT
过温
检测
驱动
IN
检测

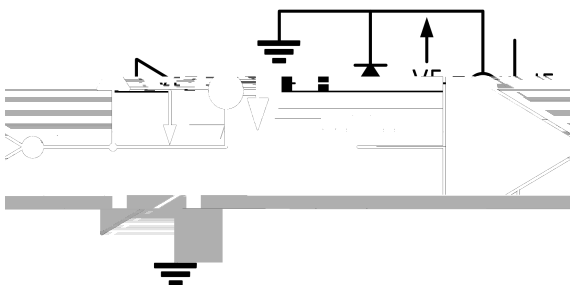

Open
VCE


Open
II(on)
VI

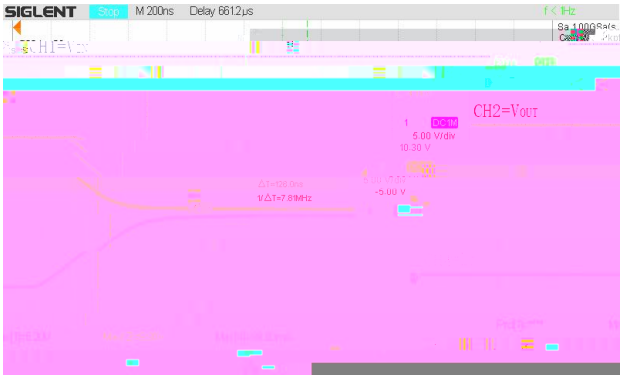

Open
VCE
Iq
VI
IC

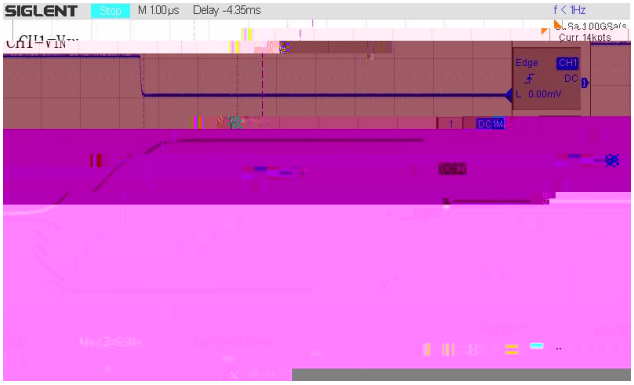

Open
II(on)
VI(on)
VCE
IC

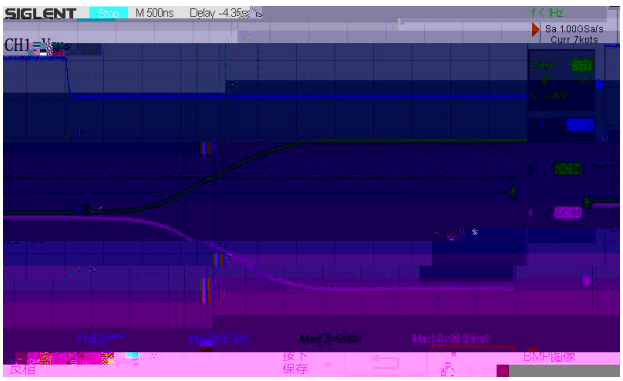

VR
IR

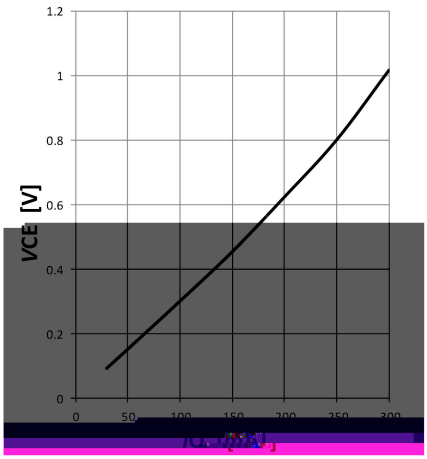


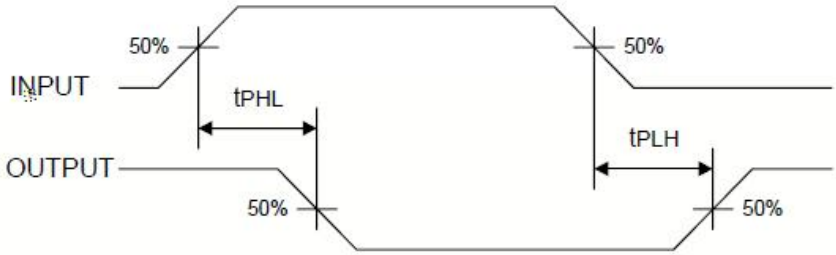
SIGLENT
Stop
M 2.00μs
Delay 661.2μs
Sa 1.00GSa/s
Curr 28kpts
CH1=VIN

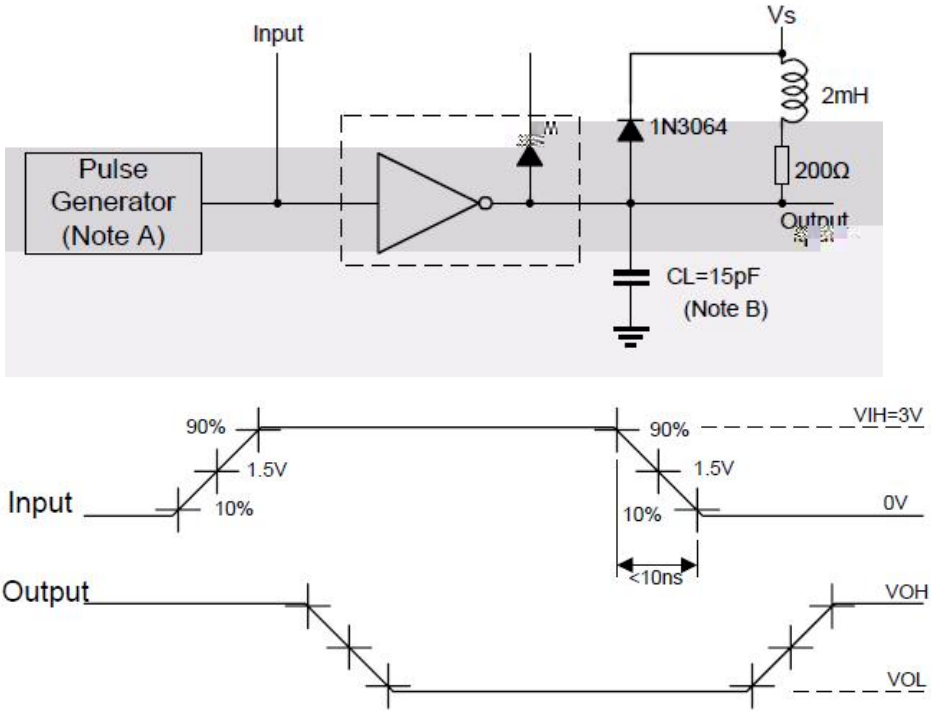

SIGLENT
Stop
M 200ns
Delay 661.2μs
CH2=VOUT
5.00 V/div
10.30 V
-5.00 V

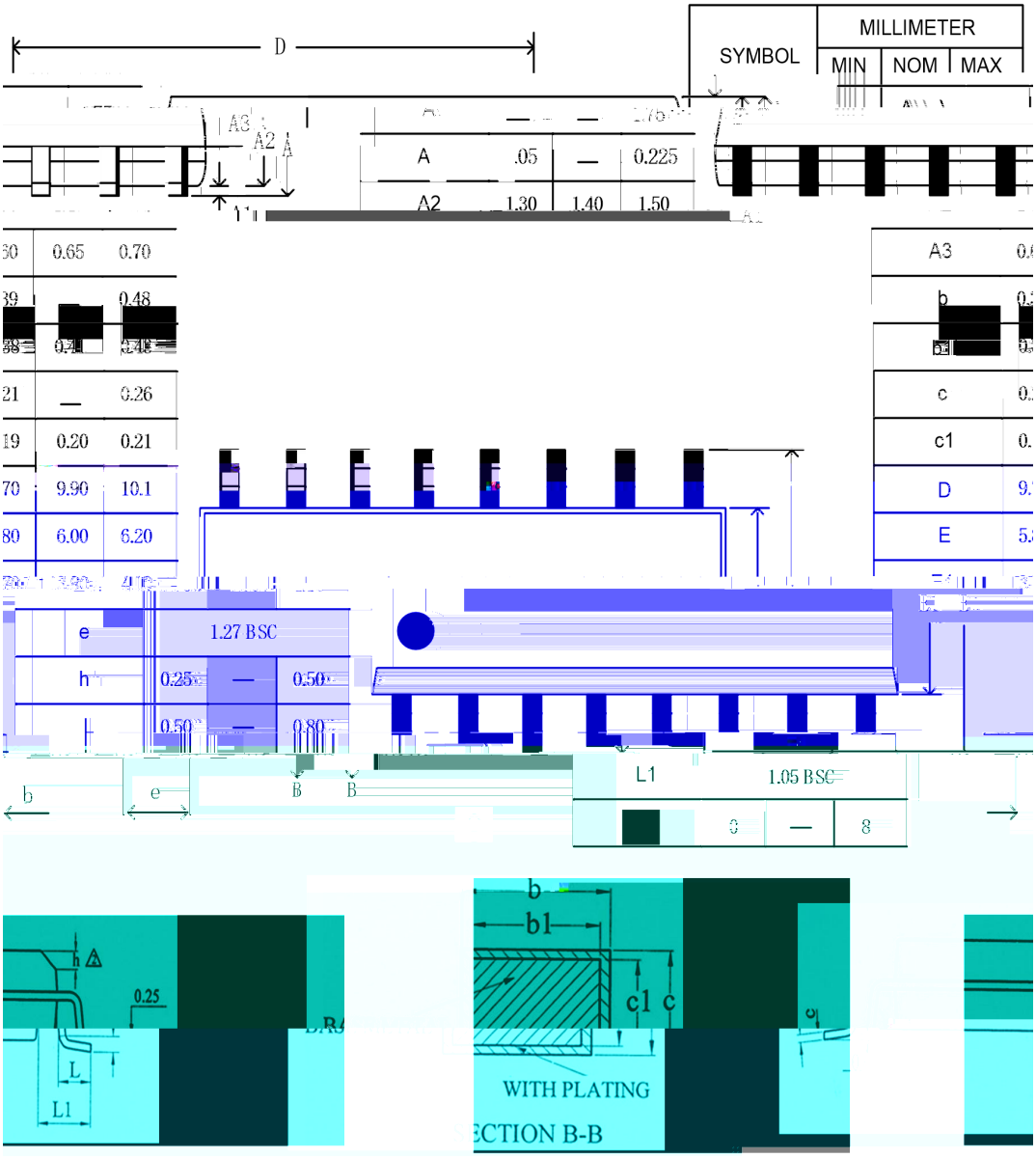

SIGLENT
Stop
M 1.00μs
Delay -4.35ms
Sa 1.00GSa/s
Curr 14kpts
Edge
CH1
L 0.00mV


SIGLENT
Stop
M 500ns
Sa 1.00GSa/s
Curr 7kpts


1.2
1
0.8
0.6
0.4
0.2
0
VCE [V]
0
50
100
150
200
250
300


50%
50%
INPUT
tPHL
tPLH
OUTPUT
50%
50%


Input
Vs
2mH
1N3064
200Ω
Pulse
Generator
(Note A)
Output
CL=15pF
(Note B)
90%
90%
VIH=3V
1.5V
1.5V
Input
10%
10%
0V
<10ns
Output
VOH
VOL


D
SYMBOL
MILLIMETER
MIN
NOM
MAX
A3
A2
A
A
.05
—
0.225
A2
1.30
1.40
1.50
0.65
0.70
0.48
0.26
0.20
0.21
9.90
10.1
6.00
6.20
A3
b
c
c1
D
E
e
1.27 BSC
h
0.25
—
0.50
0.50
—
0.80
L1
1.05 BSC
b
e
B
B
0.25
L
L1
b
b1
c1
c
WITH PLATING
ECTION B-B